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45900-000548/US

10/059,270

APPLICANT

Claus E. FURST

FILING DATE

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January 31, 2002

2819

## U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date

## FOREIGN PATENT DOCUMENTS

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## OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
	<i>PKH</i>	Peluso, Vincenzo et al. "A 900-mV Low-Power $\Delta\Sigma$ A/D Converter with 77-dB Dynamic Range." IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998; pp. 1887-1897.
		Jorgenson, Ivan et al. "Design of a 3 <sup>rd</sup> Order Micro Power Switched Current $\Delta\Sigma$ -Modulator." The Third International Conference on Electronics, Circuits and Systems, October 13-16, 1996, Vol. 2, pp. 948-951.
		Furst, Claus E. "Low Power/Low Voltage Interface Circuitry for Capacitive Sensors; Design, Optimization and Applications." Department of Information Technology, Technical University of Denmark. March 31, 1997; pp. 71-86 and pages 123-138.
	<i>▽</i>	Furst, Claus E. "A High Resolution Switched Capacitor 1bit $\Sigma\Delta$ Modulator For Low-Voltage/Low-Power Applications.", Department of Informaiton Technology, Technical University of Denmark.

Examiner:

Date Considered:

EXAMINER: Please initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.